Express Mail No.
EV132192392US

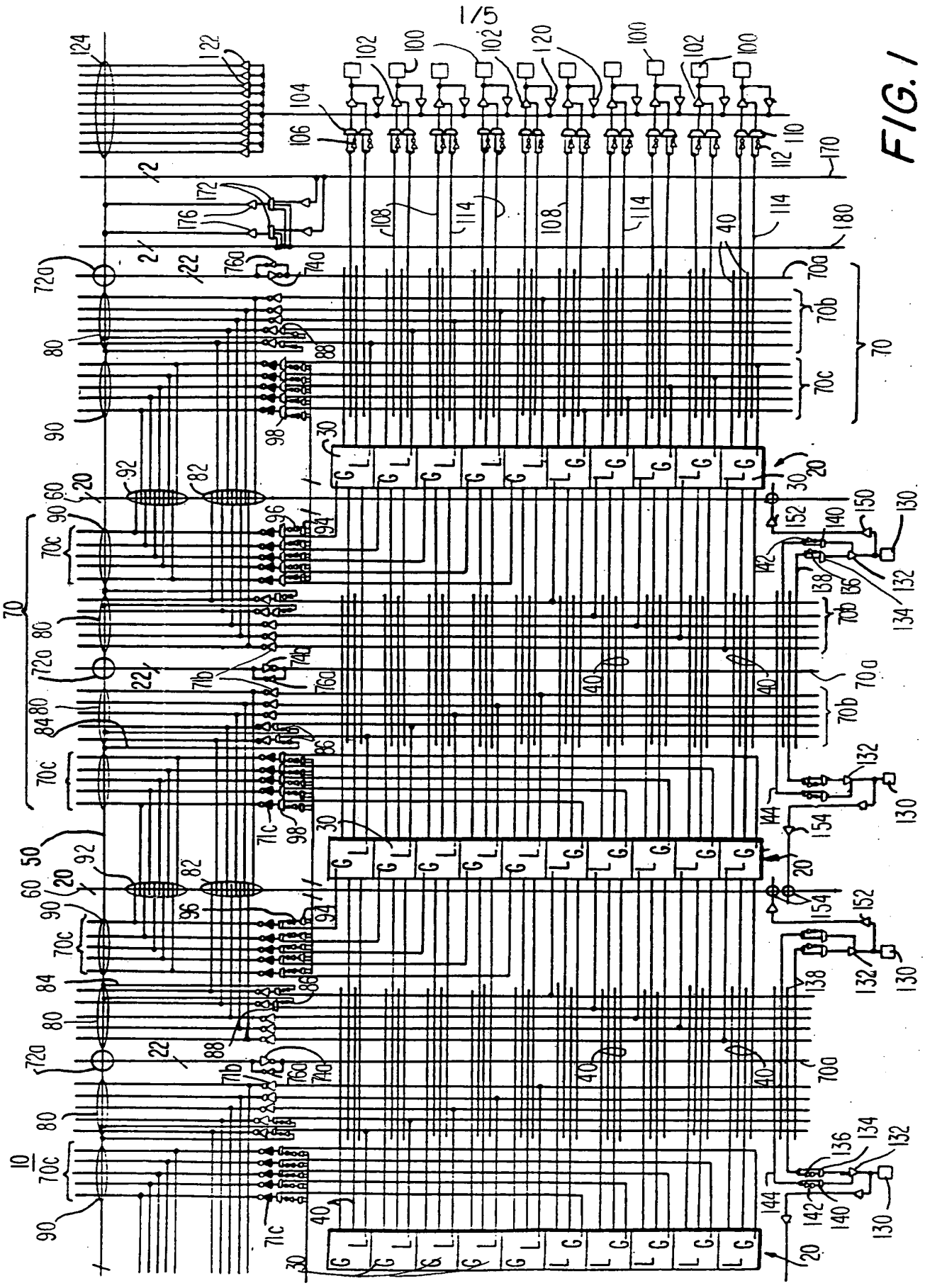
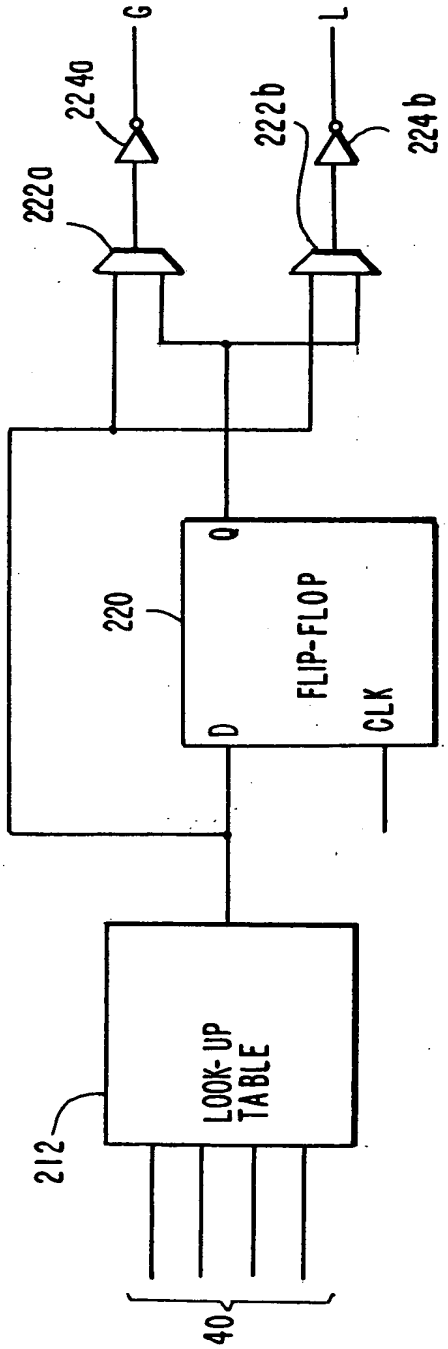


FIG. 1

FIG. 2


G
L
224a
222b
224b
222a
Q
220
FLIP-FLOP
D
CLK
30
LOOK-UP TABLE
212
40

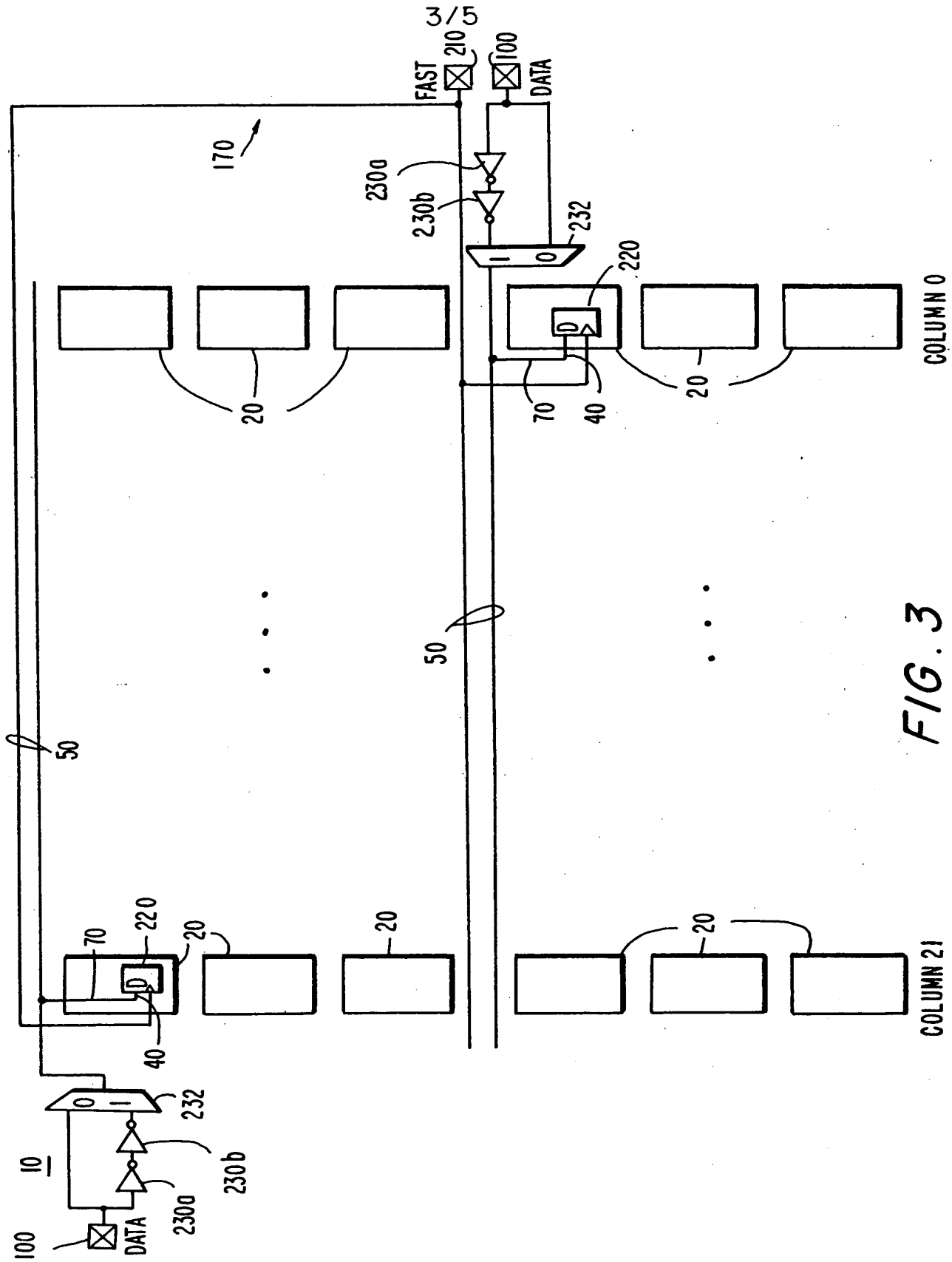
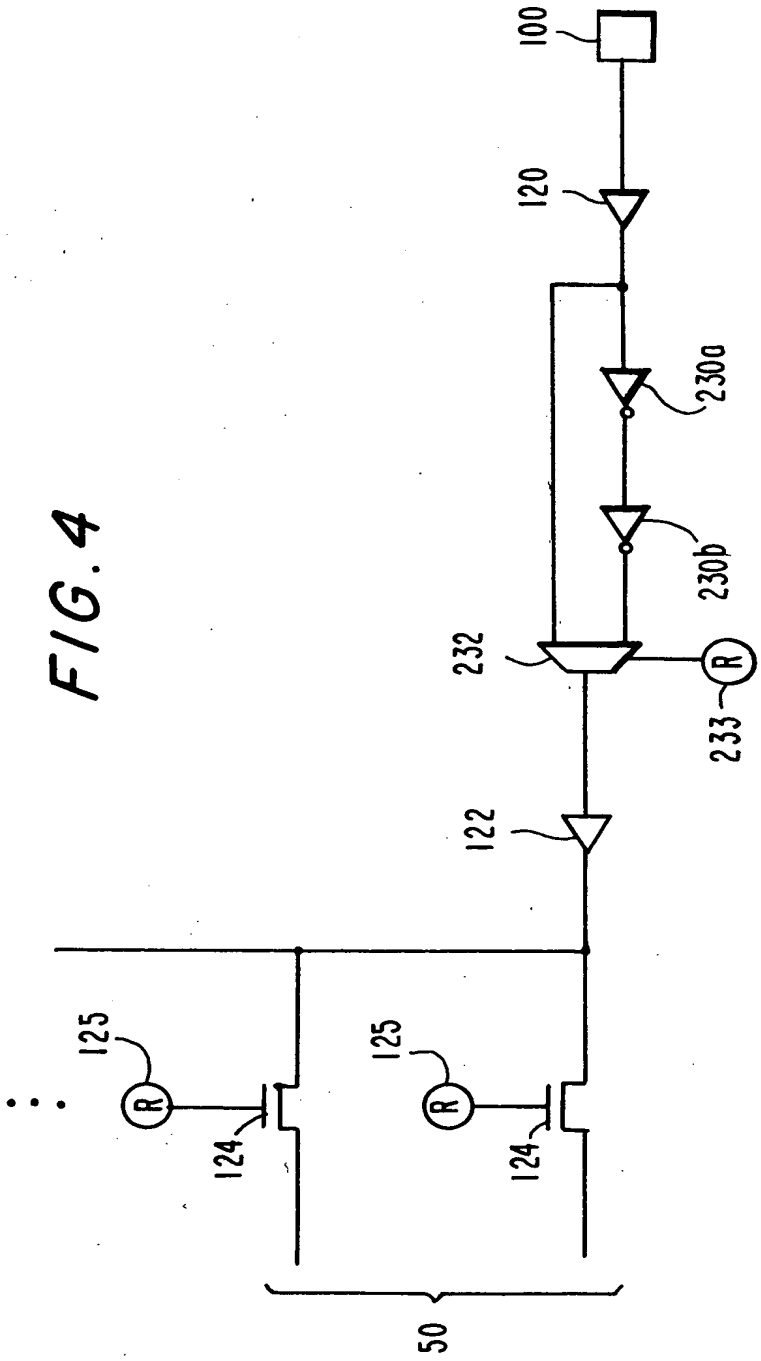

FAST
210
100
DATA
170
230a
230b
232
220
COLUMN 0
20
70
40
50
COLUMN 21
10
100
DATA

FIG. 3

FIG. 4



5/ 5

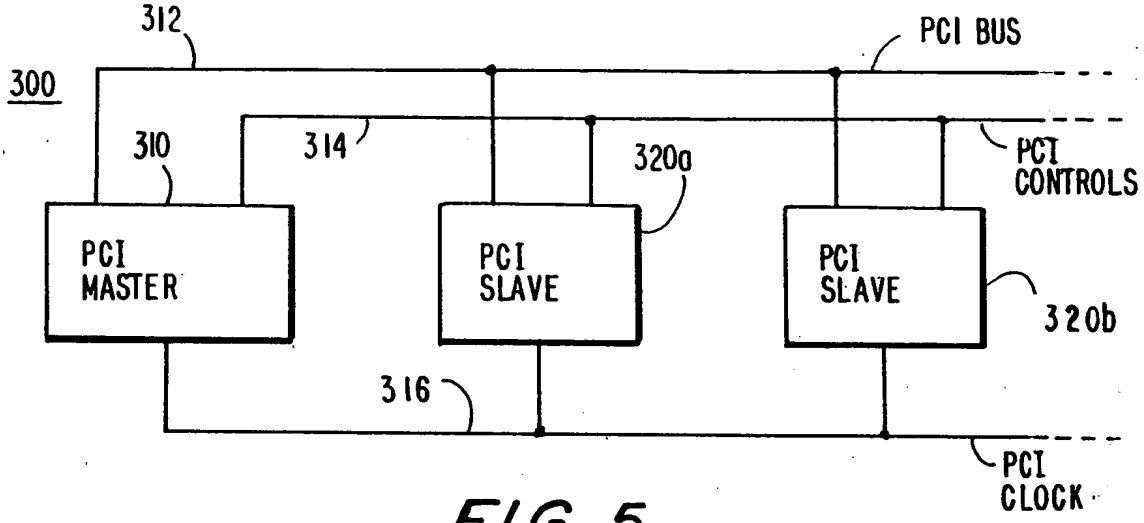

312
PCI BUS
300
310
314
320a
PCI CONTROLS
PCI MASTER
PCI SLAVE
PCI SLAVE
320b
316
PCI CLOCK

FIG. 5

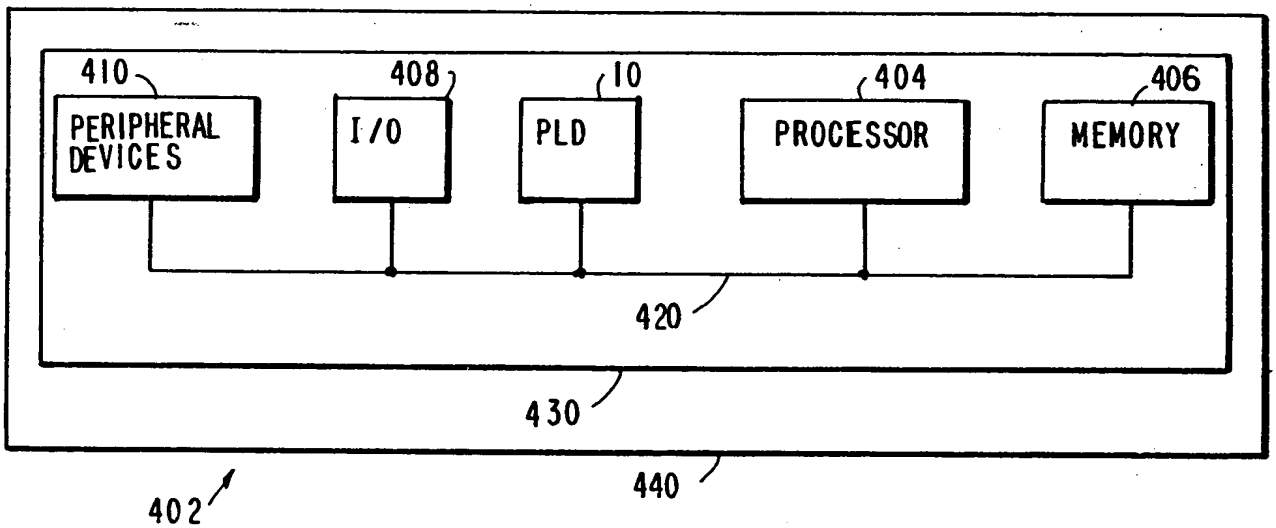

410
408
10
404
406
PERIPHERAL DEVICES
I/O
PLD
PROCESSOR
MEMORY
420
430
402
440

FIG. 6